

FIG. 1

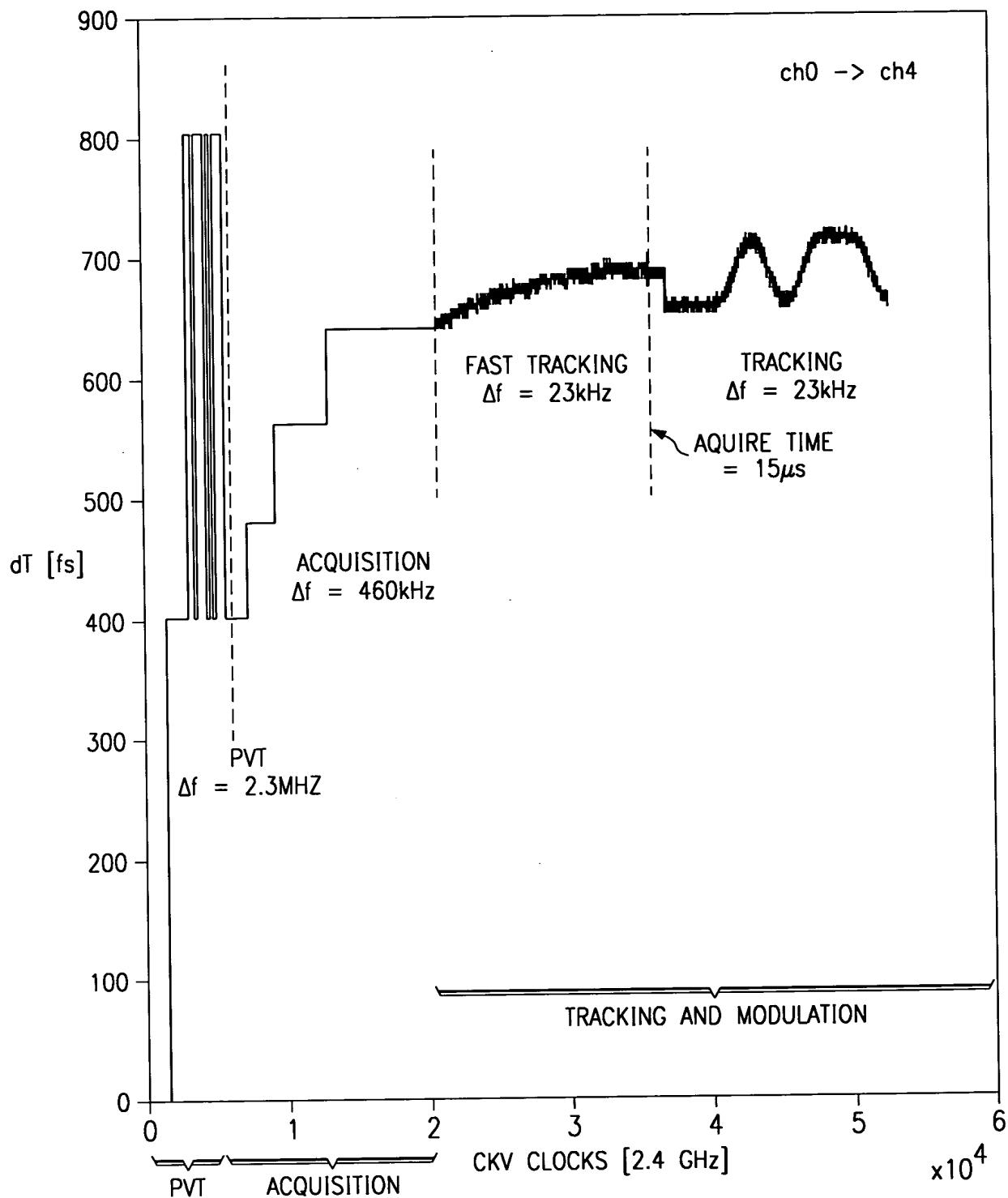


FIG. 2

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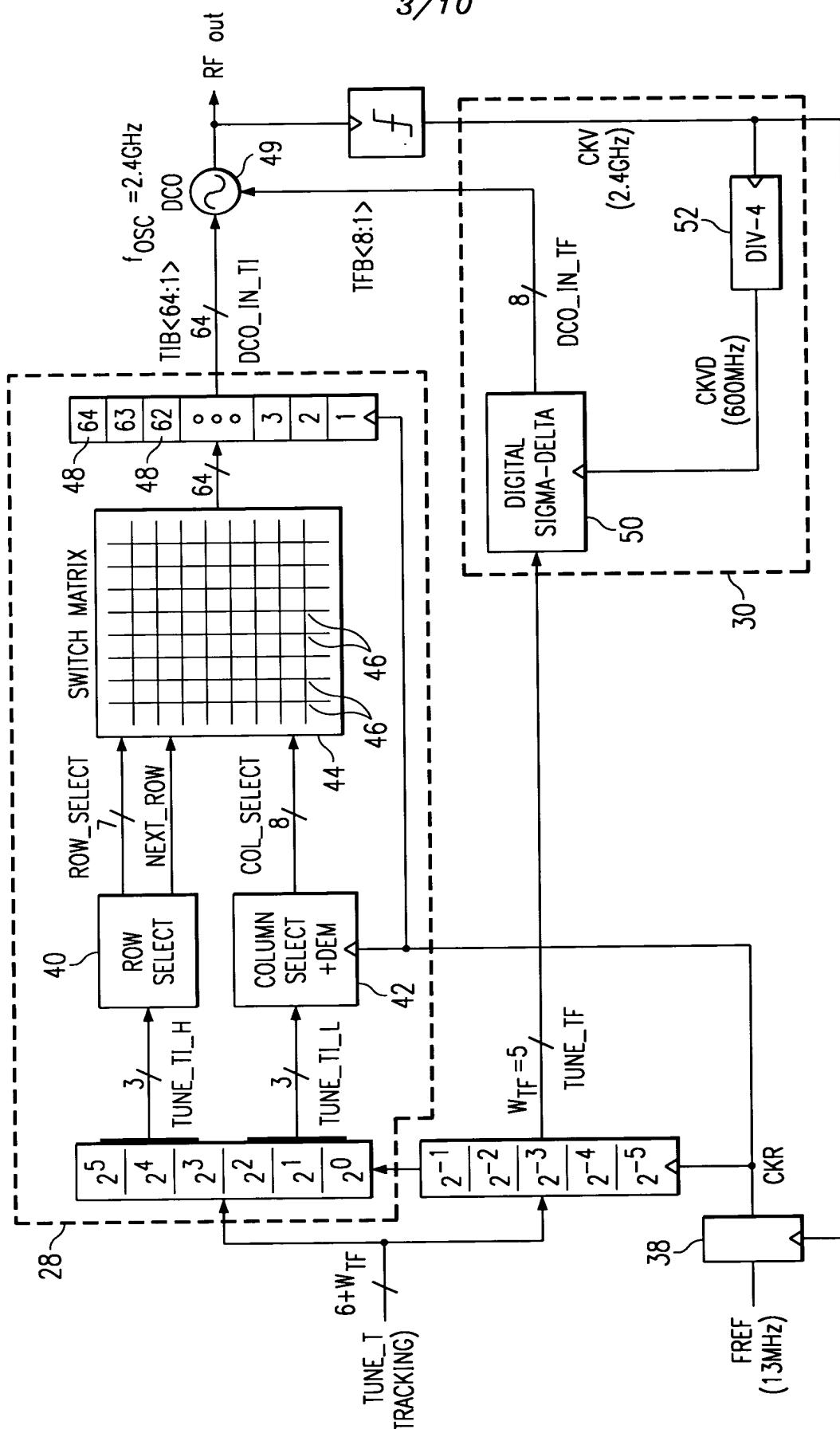
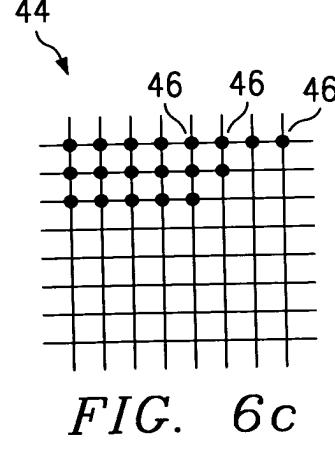
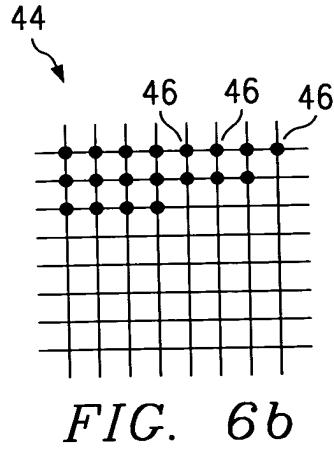
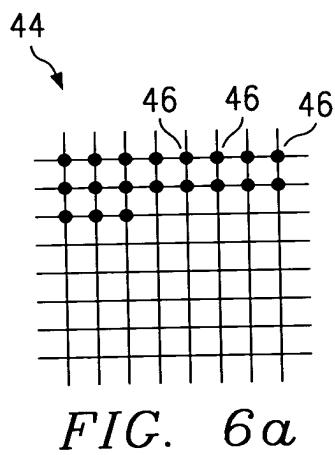
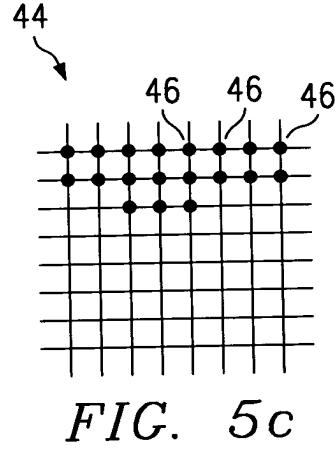
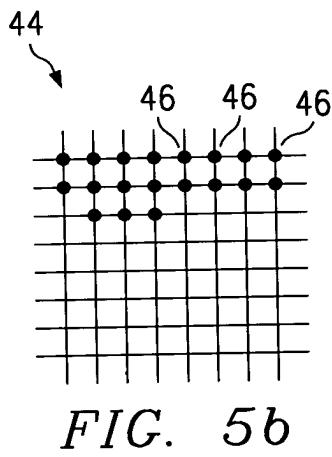
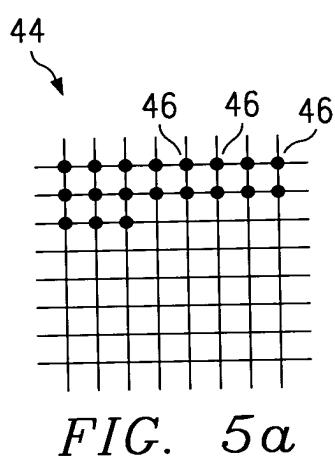
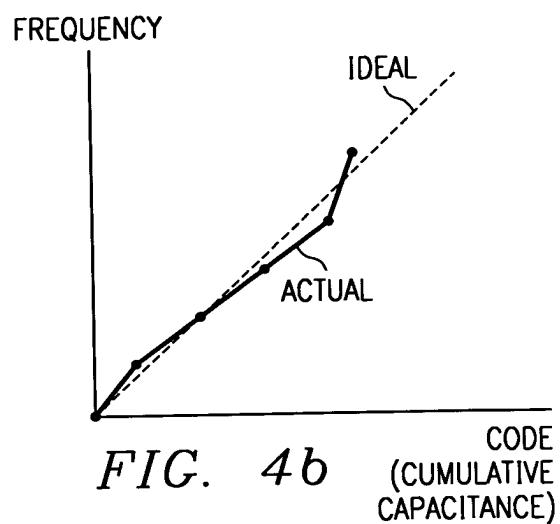
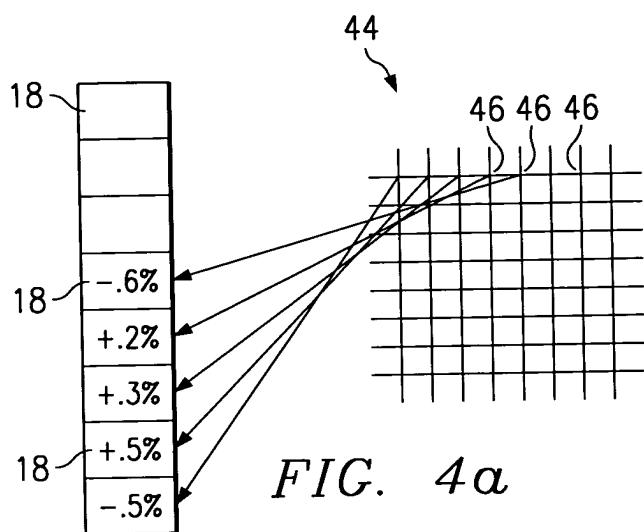


FIG. 3

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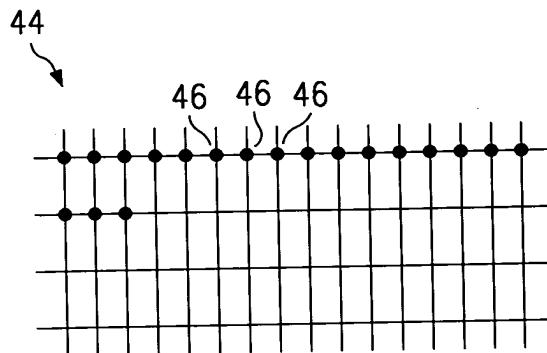


FIG. 7a

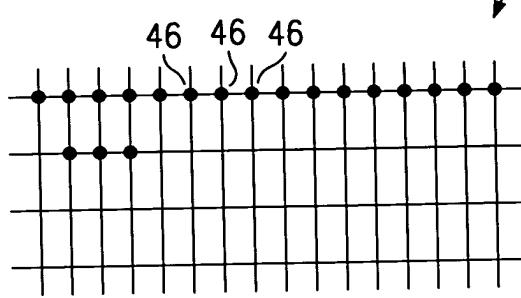
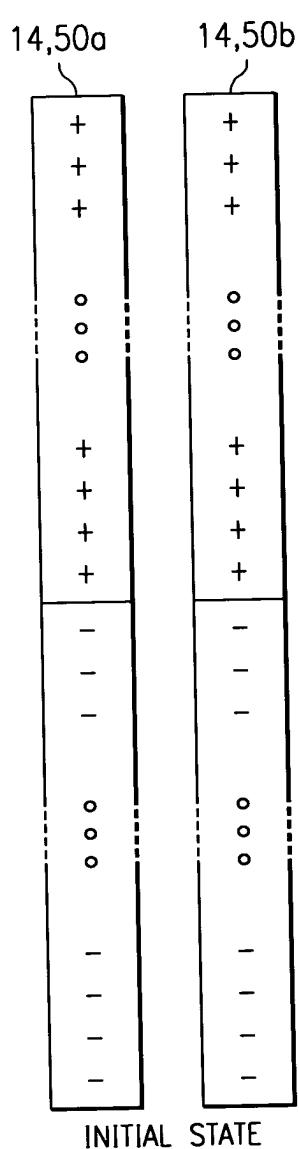
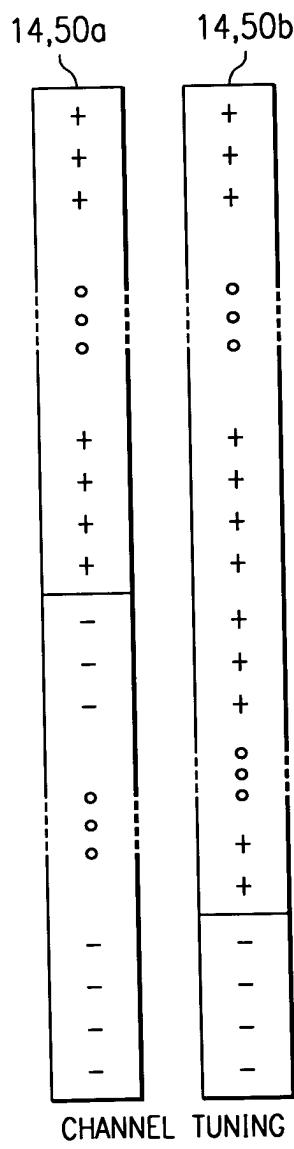


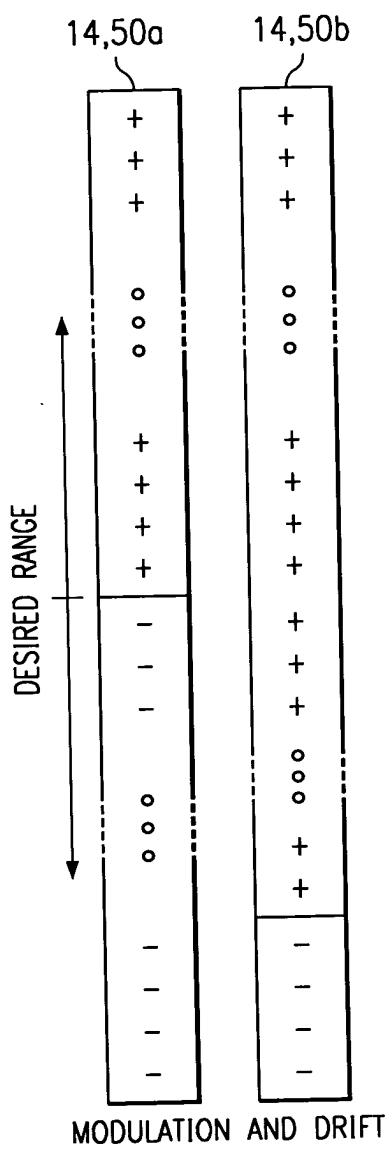
FIG. 7b



INITIAL STATE



CHANNEL TUNING



MODULATION AND DRIFT

FIG. 9a

FIG. 9b

FIG. 9c

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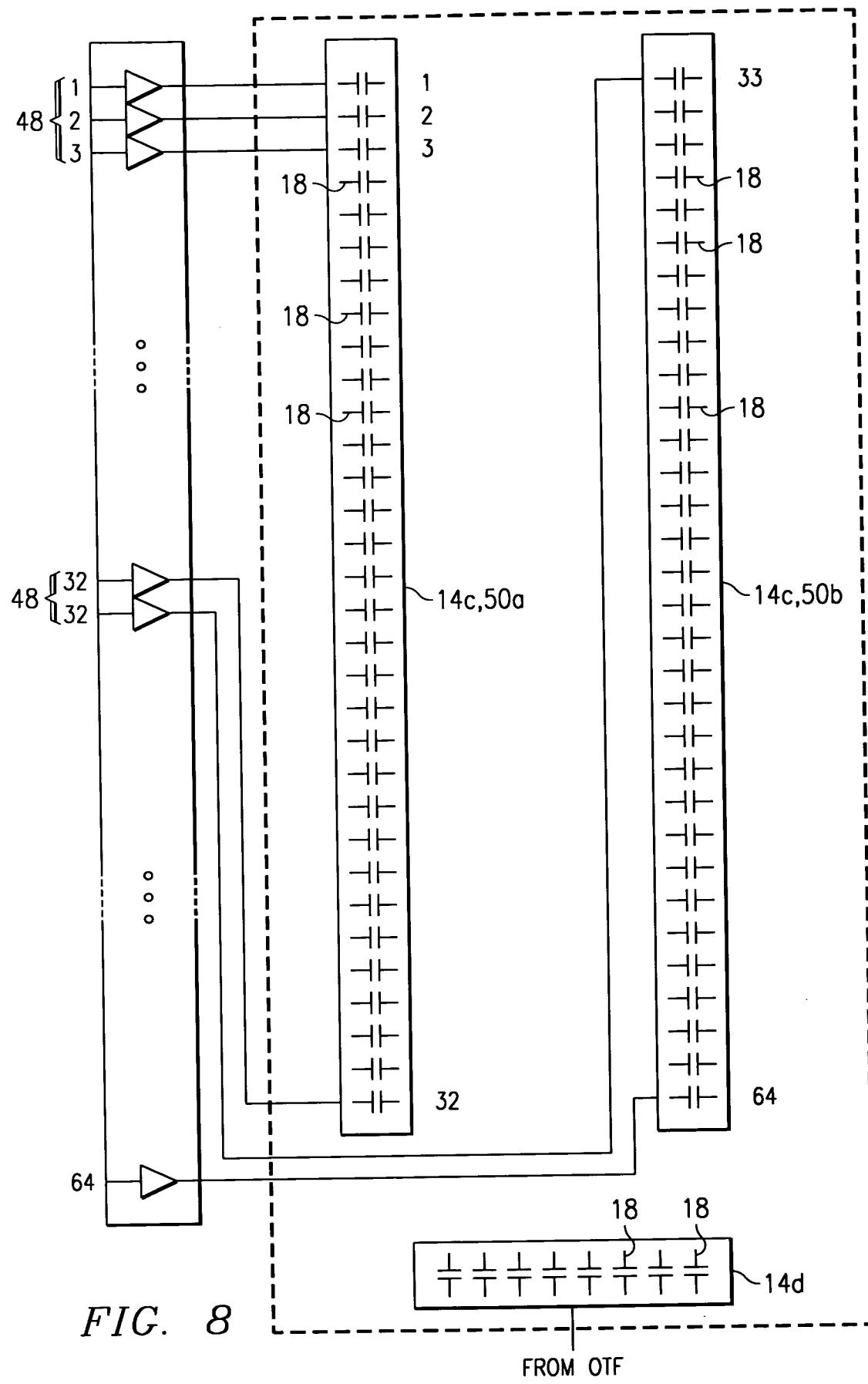


FIG. 8

FROM OTF

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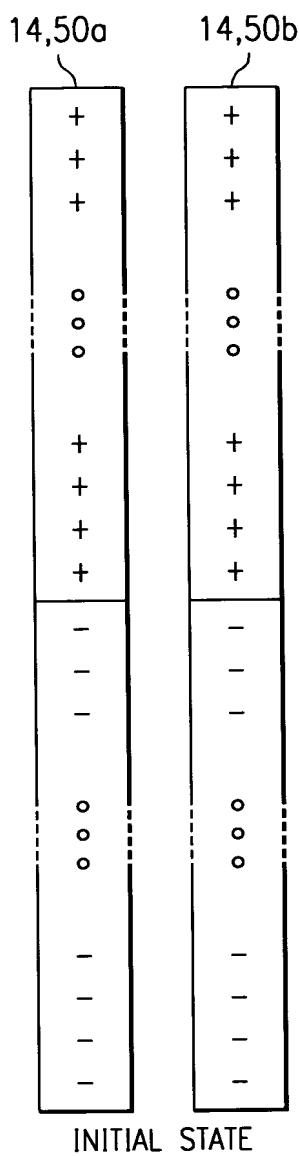


FIG. 10a

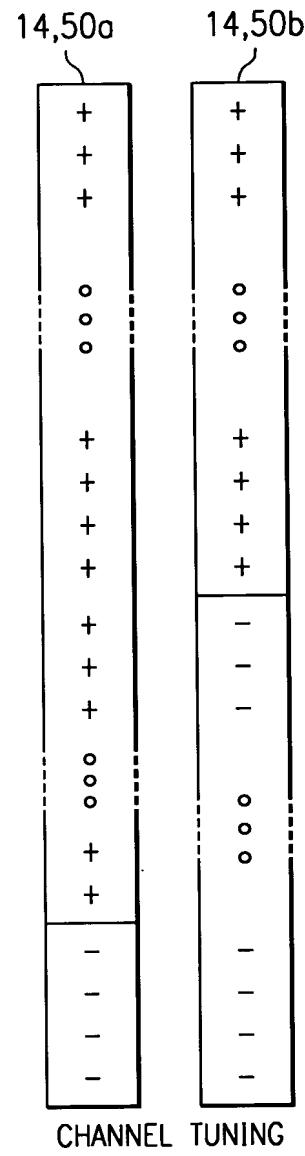


FIG. 10b

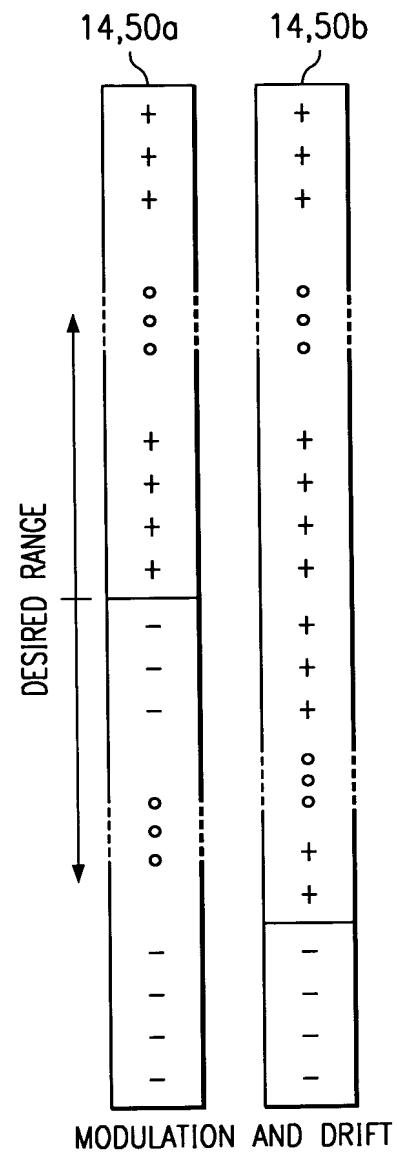


FIG. 10c

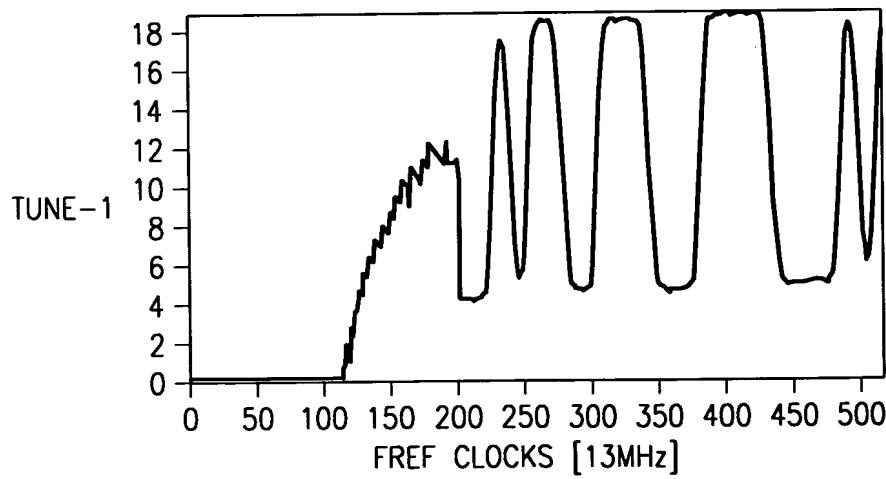


FIG. 11a

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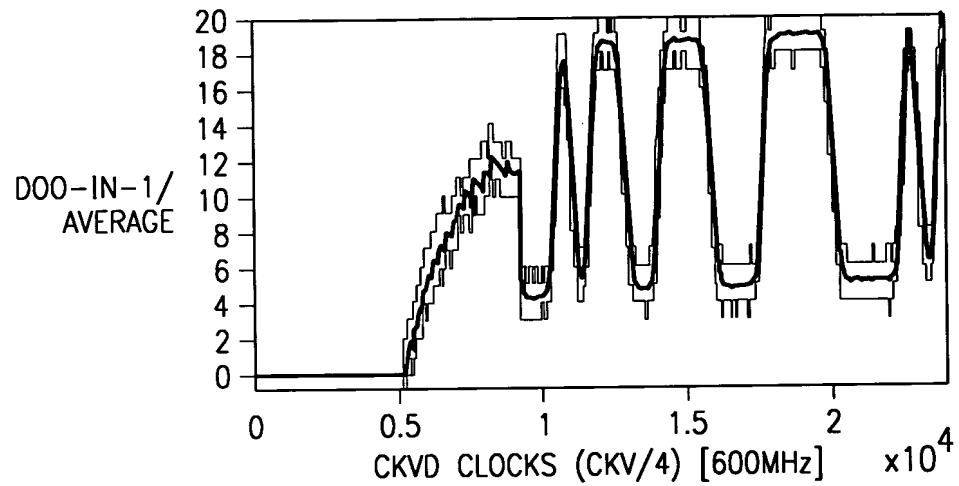
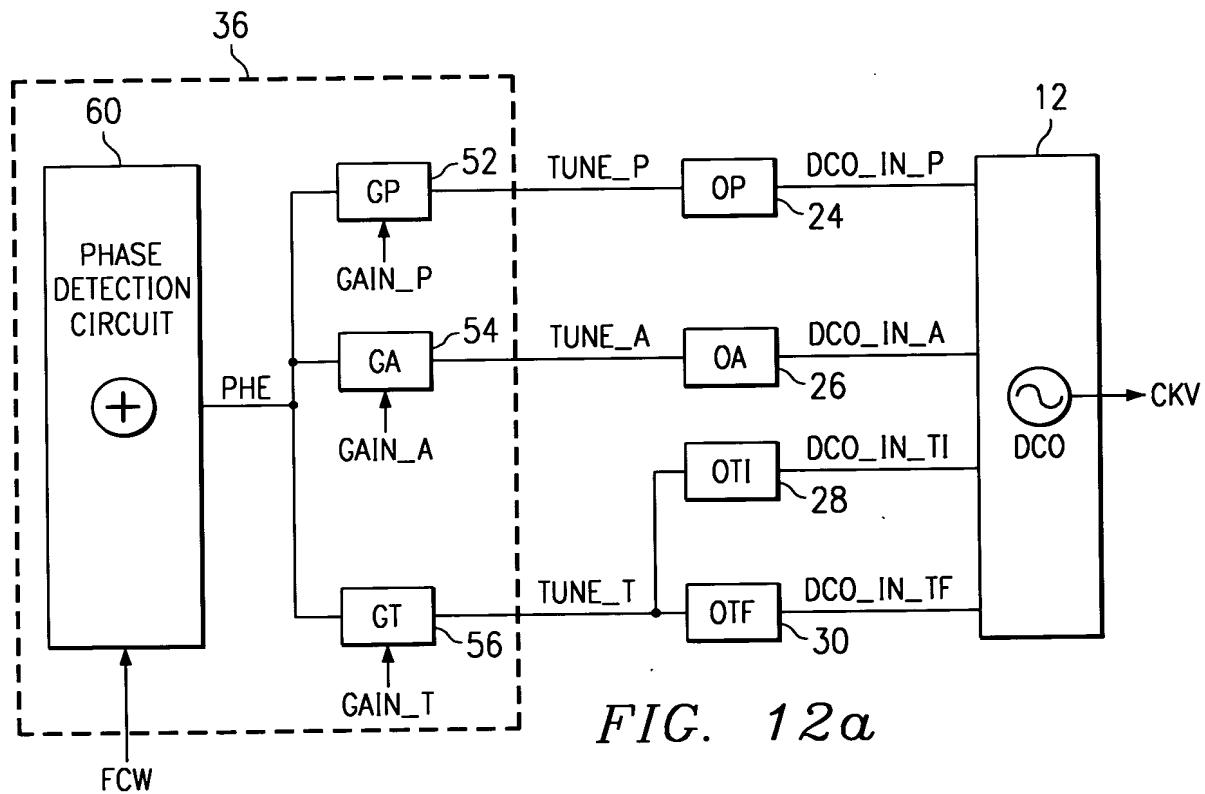


FIG. 11b



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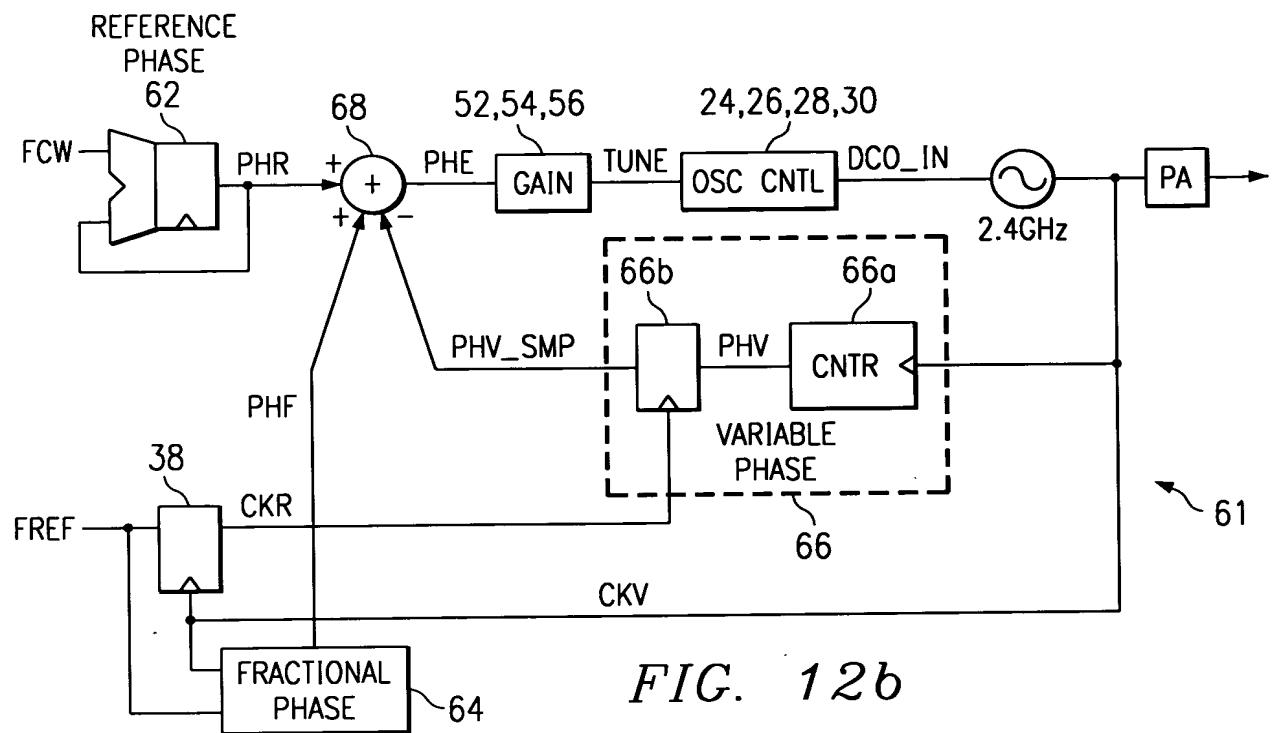


FIG. 12b

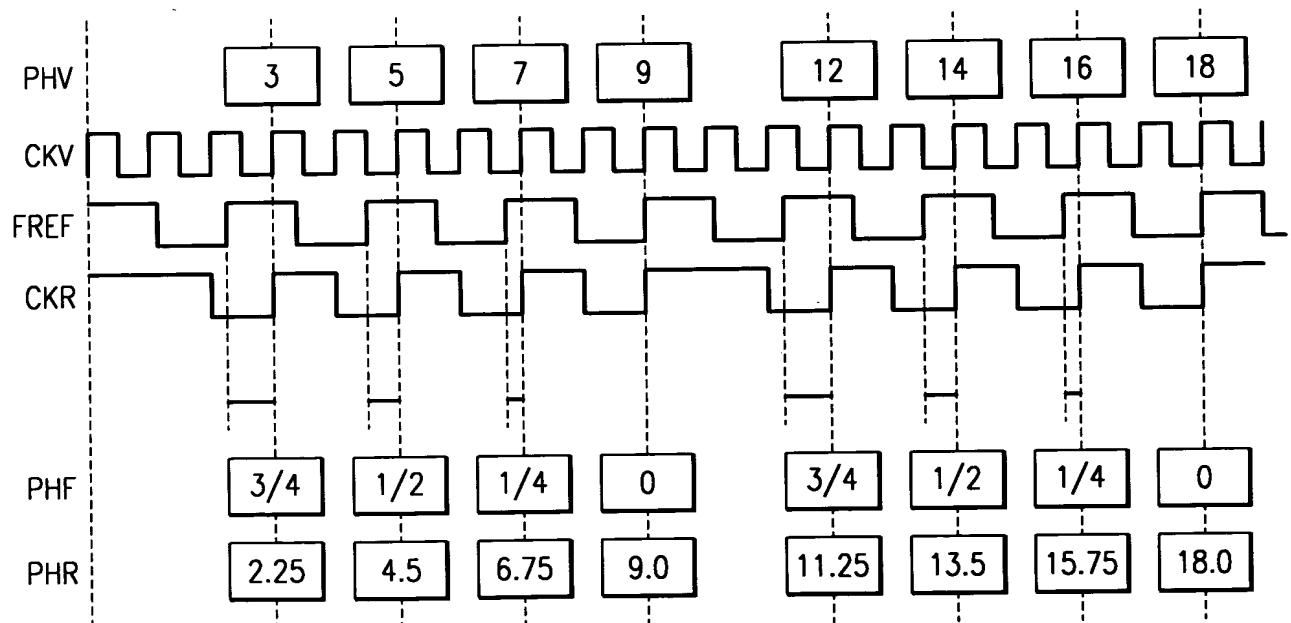


FIG. 12c

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